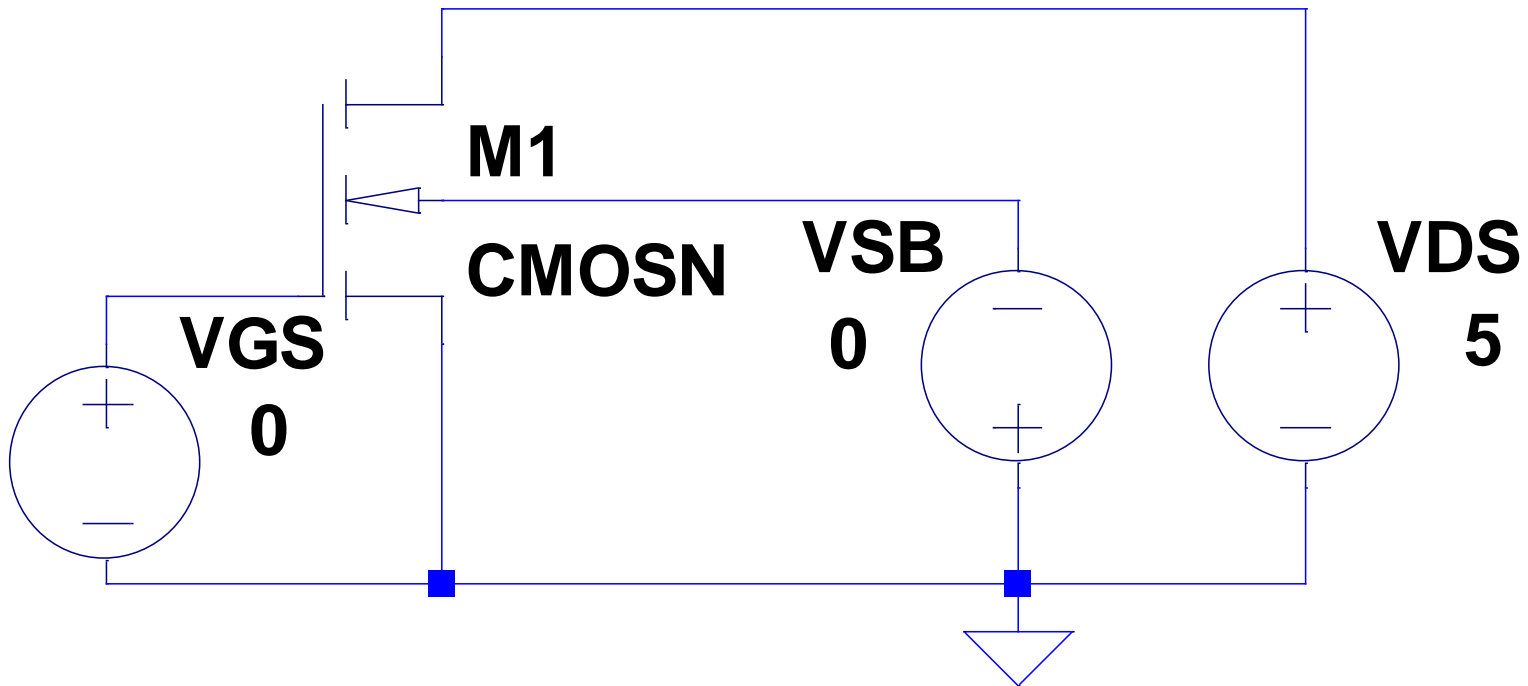


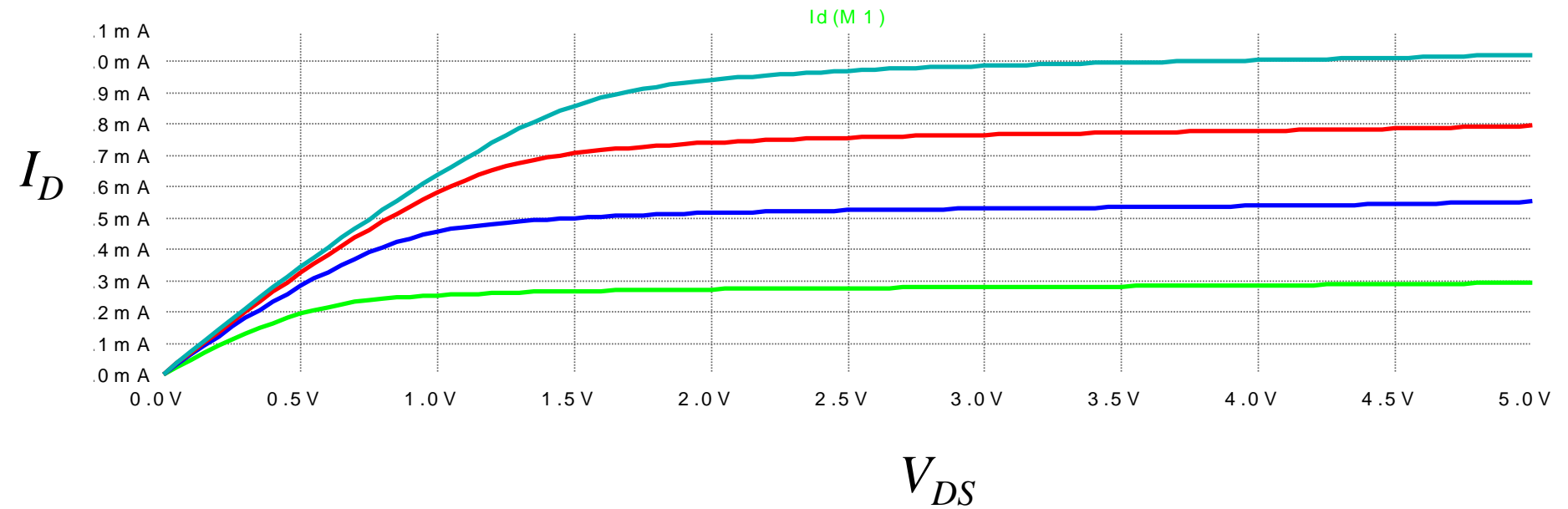
LTSpice circuit

```
.inc ON_C5.lib
```

```
.DC VDS 0 5 0.05 VGS 2 5 1
```



Characteristic Curves



SPICE netlist

```
* C:\ece531\spice3\nmos.asc
M1 N001 N003 0 N002 CMOSN l=0.6u w=2.4u
VDS N001 0 5
VSB 0 N002 0
VGS N003 0 0
.model NMOS NMOS
.model PMOS PMOS
.lib
C:\PROGRA~2\LTC\LTSPIC~1\lib\cmp\standard.mos
.inc ON_C5.lib
.DC VDS 0 5 0.05 VGS 2 5 1
.backanno
.end
```

